

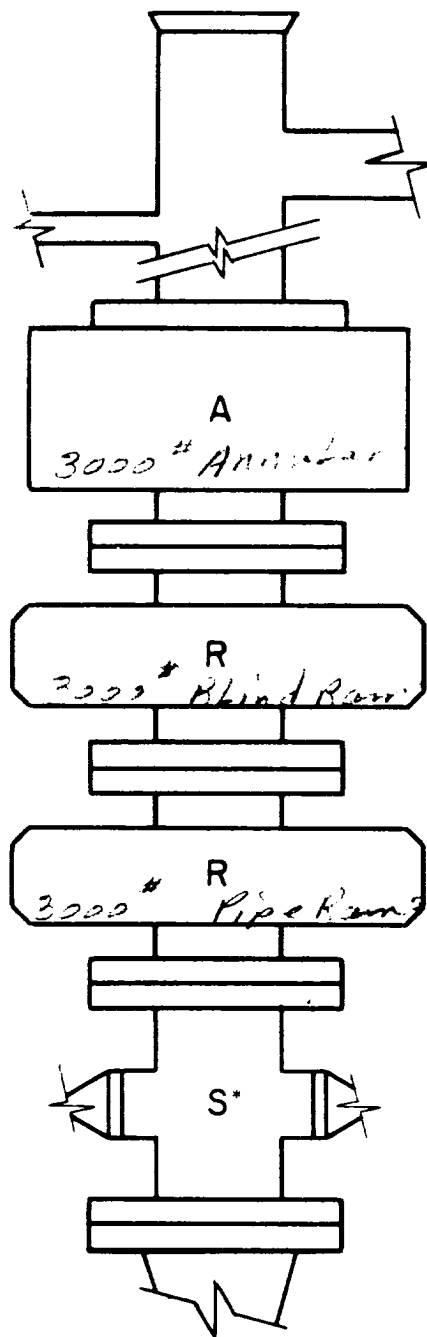


FIG. 2.C.5
 ARRANGEMENT S*RRA
 Double Ram Type Preventers, R_d, Optional.